

**EAST Search History**

<b>Ref #</b>	<b>Hits</b>	<b>Search Query</b>	<b>DBs</b>	<b>Default Operator</b>	<b>Plurals</b>	<b>Time Stamp</b>
L23	1	(mixed signal semiconductor device AND first test AND second test AND processor).clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/04/26 09:50
L24	0	(mixed signal semiconductor device AND first test AND second test AND processor AND device interface unit AND device testing unit AND control unit NAD operating system AND computational modules).clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/04/26 09:52

4/ 26/ 09 9:53:08 AM

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